



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/551,266 Group Art Unit: 2816
Filing Date: September 29, 2005 Conf.: 3385
Applicant: Toshiro AKINO Examiner: O'Toole, Colleen J.
Title: LATERAL BIPOLAR CMOS INTEGRATED CIRCUIT
Attorney Docket: 9694D-000025/US

Customer Service Window
Randolph Building
401 Dulany Street
Alexandria, VA 22314
Mail Stop Amendment

February 11, 2008

REQUEST FOR RECONSIDERATION

Sir:

In response to the Final Office Action mailed November 9, 2007, the following amendments and remarks are respectfully submitted.

Amendments to the Claims begin on page 2,

Remarks begin on page 6 of this Amendment.

	Claims remaining after Amendment		Highest number previously paid for		Present extra
Total	8	-	20	=	0
Independent	1	-	3	=	0